

# Dual Electrical Behavior of Multivalent Metal Cation-Based Oxide and Its Application to Thin-Film Transistors with High Mobility and Excellent Photobias Stability

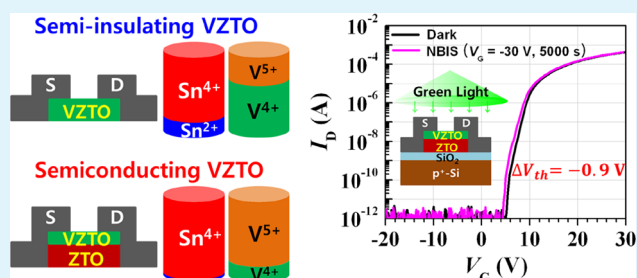
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## Supporting Information

**ABSTRACT:** The effect of multivalent metal cations, including vanadium(V) and tin (Sn), on the electrical properties of vanadium-doped zinc tin oxide (VZTO) was investigated in the context of the fabrication of thin-film transistors (TFTs) using a single VZTO film and VZTO/ZTO bilayer as channel layers. The single VZTO TFT did not show any response to the gate voltage (insulator-like behavior). On the other hand, the VZTO/ZTO bilayer TFT exhibited a typical TFT transfer characteristic (semiconducting behavior). X-ray photoelectron spectroscopy revealed that, in contrast to what is commonly true in many oxides, oxygen vacancies ( $V_O$ ) in VZTO did not provide a dominant contribution to the total carrier concentration, because the  $V_O$  peak area in the single VZTO film was 5.4% and reduced to 4.5% in VZTO/ZTO bilayer. Instead, Sn  $3d_{5/2}$  and V  $2p_{3/2}$  spectra suggest that the significant reduction in Sn and V ions is strongly related to the insulator-like behavior of the VZTO film. In negative-bias illumination tests and illumination tests with various photon energies, the VZTO/ZTO bilayer TFT had much better stability than the ZTO TFT. This result is attributed to the reduction of donor-like states ( $V_O$ ) that can be positively ionized by blue and green illumination.

**KEYWORDS:** multivalent metal cation, dual electrical property, oxide semiconductor, thin film transistor, stability



## 1. INTRODUCTION

Amorphous oxide semiconductors (AOSs), including ZnSnO (ZTO),<sup>1</sup> InZnO,<sup>2</sup> InGaO,<sup>3</sup> and InGaZnO,<sup>4</sup> have garnered much attention as channel materials for thin-film transistors (TFTs), because of their use in displays with high resolution, fast response time, large area, transparency, and/or flexibility. This practical worth of AOSs originates from the fact that even if an oxide semiconductor is deposited at a low process temperature, in order to be a thin film of amorphous phase with good uniformity, the mobility is still high (usually  $>10 \text{ cm}^2 \text{ V}^{-1} \text{ s}^{-1}$ ), unlike the previous channel material (amorphous Si).<sup>4</sup> Furthermore, AOSs have the benefit of being transparent. Among various AOS TFTs, current commercialized AOS TFTs adopt InGaZnO as the channel material. There are two reasons for this. First, among AOSs, IGZO is the most studied and the most well understood. Second, the IGZO TFT has sufficient reliability for mass production. In the meantime, ZTO TFTs have also drawn considerable attention, because of their comparable or better TFT performance and much lower raw material costs when compared with InGaZnO TFTs. However, unlike InGaZnO TFTs, the inferior stability of ZTO TFTs under a variety of stresses such as bias, temperature, illumination, and their combinations still remains a major obstacle for commercialization. In particular, the negative-bias illumination stress (NBIS) is a significant issue that must be

overcome for applications in active matrix liquid crystal displays (AMLCDs) or transparent active matrix organic light-emitting diode (AMOLED) displays, since, for most operation times, the TFTs are stressed by a negative gate bias and are exposed to visible light emitted by the underlying backlight unit in AMLCDs or ambient light in transparent AMOLED displays.<sup>5,6</sup>

The stability can be improved by suppressing instability events of the channel-layer that include: (i) trapping of carriers or accumulation of charged defects at the gate insulator/semiconductor interface;<sup>7,8</sup> (ii) defect generation in the bulk semiconductor;<sup>9–11</sup> and (iii) interaction with ambient gases at the back surface of the channel layer.<sup>12,13</sup> Therefore, researchers have tried many methods such as plasma or ozone treatment of the gate insulator/semiconductor interface or the back surface,<sup>14–16</sup> passivation of the back surface,<sup>12,13</sup> insertion of an ultrathin positive charge barrier,<sup>17</sup> channel-thickness control,<sup>15,17,18</sup> multistacking of the channel layer,<sup>19–22</sup> and additional element incorporation (called “doping” here).<sup>23–25</sup> The main goal of these methods is the control of defects such as oxygen vacancies, impurities, and back-surface-adsorbed chemicals. Among defects that possibly exist in AOSs, oxygen

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vacancies ( $V_O$ ) have been recognized as the main cause of the NBIS instability.<sup>8–11,26–28</sup>  $V_O$  can be ionized to  $V_O^{2+}$  and can donate two carrier electrons to the channel during light illumination. This increased carrier concentration results in a negative shift of the threshold voltage ( $V_{th}$ ). Moreover, the doubly ionized oxygen vacancies ( $V_O^{2+}$ ) can move to the gate insulator/semiconductor interface when exposed to a negative bias and can be accumulated or trapped at the interface. These  $V_O^{2+}$  at the interface cause downward band bending near the interface and, consequently, lead to a negative shift in  $V_{th}$ . Thus, some metal cations, such as Hf,<sup>23</sup> Ga,<sup>24</sup> and Zr,<sup>25</sup> have been considered as additional elements to suppress the formation of  $V_O$  and to some extent, relatively improved stability was reported. However, the suppression of  $V_O$  also led to the severe degradation of mobility and on-current, because the  $V_O$  also acts as a carrier generator, and the mobility of an AOS is generally proportional to the carrier concentration.<sup>29</sup> Therefore, bilayer structures such as HIZO (0.15 mol % HfO<sub>2</sub>)/HIZO (0.23 mol % HfO<sub>2</sub>),<sup>19</sup> HIZO/IZO,<sup>20</sup> and ZTO/IZO<sup>22</sup> have been designed as a trade-off strategy to catch both high mobility and high stability and showed relatively good performance and stability.

Among unexploited doping elements in ZTO material system, vanadium (V) is well worth considering as a  $V_O$  suppressor in ZTO material system, based on the previous results of first-principle calculation in which the Gibbs free energy for the formation of  $V_O$  in VO<sub>2</sub> was 4.83 eV and was <3.13 eV in ZnO or <3.49 eV in SnO<sub>2</sub>.<sup>30,31</sup> Also, the bond-dissociation energy that is required to dissociate the bond between vanadium and oxygen is larger than the cases of Zn or Sn (V–O: 637 kJ/mol, Zn–O: ≤ 250 kJ/mol, Sn–O: 528 kJ/mol);<sup>32</sup> this fact raises our expectation about elemental vanadium (V) as a possible  $V_O$  suppressor. In addition, like the Sn atom, which has two ionic forms (Sn<sup>4+</sup> and Sn<sup>2+</sup>), V atoms are multivalent and can be V<sup>5+</sup>, V<sup>4+</sup>, V<sup>3+</sup>, and V<sup>2+</sup>. In fact, besides actinides and lanthanides, there are many multivalent metal elements:<sup>33</sup>

Ti (+2, +3, +4)  
 V (+2, +3, +4, +5)  
 Cr (+2, +3, +6)  
 Mn (+2, +3, +4, +6, +7)  
 Fe (+2, +3, +6)  
 Co (+2, +3)  
 Cu (+1, +2)  
 Nb (+3, +5)  
 Mo (+2, +3, +4, +5, +6)  
 Ru (+3, +4, +5, +6, +8)  
 Pd (+2, +4)  
 Sn (+2, +4)  
 Re (+2, +4, +5, +6, +7)  
 Os (+2, +3, +4, +6, +8)  
 Ir (+2, +3, +4, +6)  
 Pt (+2, +4, +6)  
 Au (+1, +3)  
 Hg (+1, +2)  
 Tl (+1, +3)  
 Pb (+2, +4)

(the numbers in the parentheses indicate possible oxidation states). However, although their different charge states in an oxide material may affect overall electrical properties, so far, most of the previous reports have explained the origin of

electrical conductivity of an oxide semiconductor as point defects, such as  $V_O$ , interstitials (Zn<sub>i</sub>, H<sub>i</sub>, etc.),<sup>27,34–37</sup> or composition ratio of metal cations, in particular, the indium ratio in InZnO,<sup>28</sup> InGaZnO,<sup>29</sup> HfInZnO,<sup>38</sup> etc. In fact, there are few studies about the effect of the charge states of multivalent metal cations (including V and Sn) on the electrical properties of the metal oxide semiconductor.

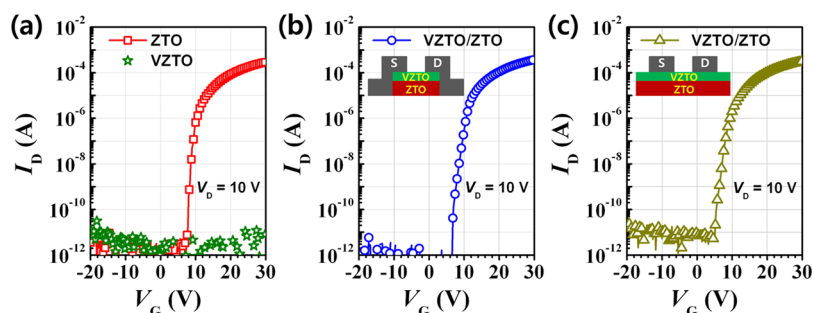
In this study, we first introduce the multivalent V cation as a dopant into the ZTO channel layer and demonstrate that a ZTO-based bilayer TFT with high mobility and high stability can be fabricated by adopting a V-doped ZTO (VZTO)/ZTO stack as a channel structure. We also report a surprising result that the VZTO films have a dual electrical property, depending on channel structure: (i) a single VZTO film behaves like a semi-insulator, and (ii) the top VZTO film in the VZTO/ZTO stack shows semiconducting properties appropriate to a channel layer of TFT. X-ray photoelectron spectroscopy (XPS) results revealed that the multivalent cations, Sn (Sn<sup>4+</sup> or Sn<sup>2+</sup>) and V (V<sup>5+</sup> or V<sup>4+</sup>) were responsible for the dual electrical property of the VZTO films.

## 2. EXPERIMENTAL PROCEDURE

**2.1. TFT Fabrication.** We fabricated inverted-staggered bottom-gate TFTs with three different channel layers (ZTO, VZTO, and VZTO/ZTO) on heavily doped *p*-type Si substrates with 200-nm-thick thermally grown SiO<sub>2</sub> as a gate insulator. All channel layers were deposited on the substrates at room temperature via radio frequency (RF) magnetron sputtering. First, we used a 4-in. ZTO target with a Zn:Sn atomic ratio of 4:1 for the deposition of the ZTO layer. Second, when depositing the VZTO layer, vanadium was doped by a co-sputtering method using both ZTO and V<sub>2</sub>O<sub>3</sub> targets. Last, for the deposition of the VZTO/ZTO bilayer, the VZTO film was co-sputtered *in situ* on the sputtered ZTO film. The channel region was defined by a conventional photolithography and wet etching process. All the defined films were thermally annealed at 350 °C in N<sub>2</sub> gas for 3 h. The Ti/Au/Ti (10/80/10 nm) source and drain (S/D) electrodes were formed by electron beam evaporation at room temperature. Shadow masks were used to define the S/D electrodes, and the channel length (*L*) and width (*W*) were 50 and 500 μm, respectively. No passivation layers were deposited. Finally, all the TFTs were thermally annealed at 250 °C in ambient air for 30 min.

**2.2. Characterizations of Thin Films.** The thickness of the channel layers was checked by a surface profilometer (XP-100, Ambios Technology, Inc.). The V:Zn:Sn composition ratios of the ZTO and VZTO were verified by energy-dispersive spectroscopy (EDS) and were 0:91.6:18.4 and 7.0:77.2:15.8, respectively. Chemical analysis of the films was performed using X-ray photoelectron spectroscopy (XPS) (Theta Probe, Thermo Fisher Scientific Co.).

**2.3. TFT Measurements.** All the current–voltage (*I*–*V*) measurements were carried out at room temperature in air using a Model HP-4145B semiconductor parameter analyzer. For light illumination, a 150 W Xe arc lamp (LS-150, ABET Technologies, Inc.) and a monochromator (Monora 200, DONGWOO OPTRON Co., Ltd.) were used. The optical power of the monochromatic light was measured using a UV-enhanced Si detector and was controlled to be 0.1 mW cm<sup>−2</sup> on the TFT devices.



**Figure 1.** Transfer characteristics of (a) ZTO and VZTO TFTs (b, c) VZTO/ZTO TFTs. The insets in panel (b) or panel (c) show that the S/D electrodes of the VZTO/ZTO TFTs are in contact with both VZTO and ZTO or only with VZTO, respectively.

### 3. RESULTS AND DISCUSSION

**3.1. Anomalous Electrical Behavior of VZTO Thin Films.** Figure 1a shows the transfer characteristics of single-layer TFTs using ZTO and VZTO films as channel materials with a thickness of 80 nm each. Here,  $V_{th}$  was defined as the gate voltage ( $V_G$ ), which induces a drain current ( $I_D$ ) of 1 nA, and the field effect mobility ( $\mu_{FE}$ ) was determined from the transfer data obtained at a low drain voltage ( $V_D = 1$  V) by the following equation:

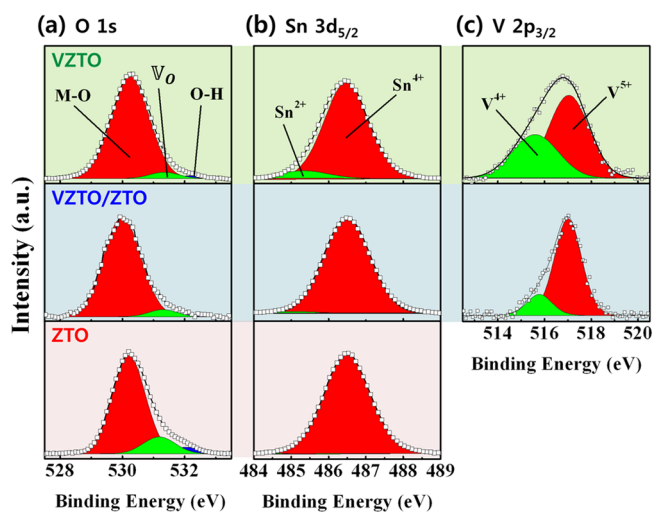
$$\mu_{FE} = \left[ \frac{L}{C_i W V_D} \frac{dI_D}{dV_G} \right]_{\max} \quad (1)$$

where  $C_i$ ,  $W$ , and  $L$  are the gate insulator capacitance per unit area, channel width and length, respectively. The subthreshold swing (SS) value was estimated by

$$SS = \left[ \left( \frac{d \log I_D}{dV_G} \right)_{\max} \right]^{-1} \quad (2)$$

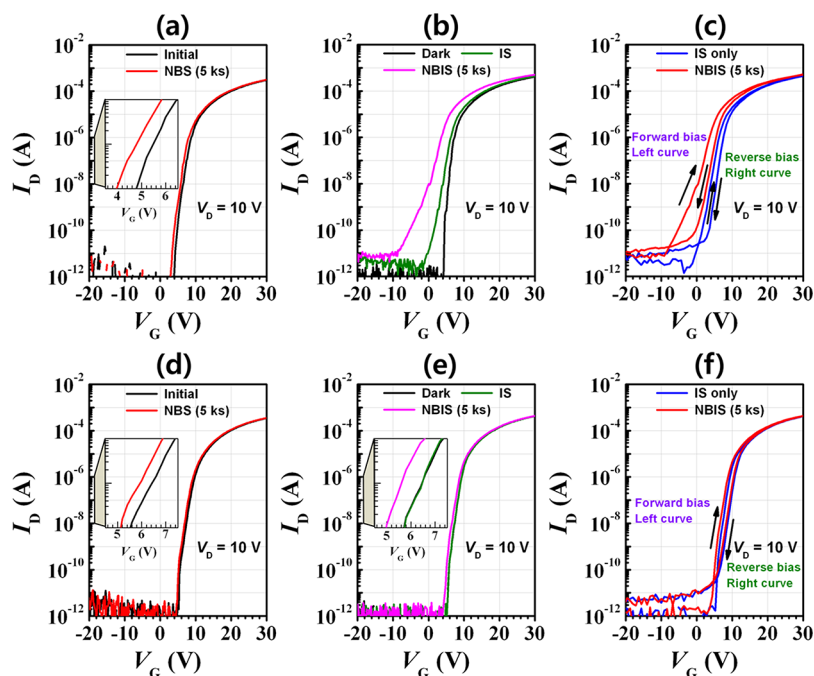
The single ZTO TFT as a reference exhibited values of  $V_{th} = 8.3$  V,  $\mu_{FE} = 15.3$  cm<sup>2</sup> V<sup>-1</sup> s<sup>-1</sup>, and SS = 0.24 V dec<sup>-1</sup>. On the other hand, the single VZTO TFT did not have any TFT characteristics. Even at  $V_G = 80$  V, no charge carrier accumulation occurred. In this study, we could not measure Hall effect characteristics of the vanadium-doped ZTO thin film, because it had a high electrical resistivity. This indicates that the single VZTO film is a semi-insulator, even though it has an optical band gap of 3.2 eV (see Figure S1 in the Supporting Information). On the other hand, the VZTO/ZTO bilayer TFT shown in Figure 1b exhibited a high electrical performance comparable to that of the single ZTO TFT. Specifically, it showed values of  $V_{th} = 7.7$  V,  $\mu_{FE} = 16.9$  cm<sup>2</sup> V<sup>-1</sup> s<sup>-1</sup>, and SS = 0.44 V dec<sup>-1</sup>. Here, the electrical transport of the VZTO/ZTO bilayer TFT can be expected to originate from the bottom ZTO layer, because, as shown in the inset of Figure 1b, the S/D electrodes still contact the sides of the bottom ZTO layer, even though the top VZTO layer does not provide an effective conduction path. Thus, in order to confirm whether or not the top VZTO layer in the VZTO/ZTO TFT contributes to charge transport, we fabricated a VZTO/ZTO TFT in which the S/D electrodes only contact the top VZTO layer (40 nm thick). The schematic TFT structure is illustrated in the inset of Figure 1c. This TFT shows the transfer characteristic of a typical TFT. Thus, it is noticeable that the VZTO film in the VZTO/ZTO bilayer has a semiconducting property, unlike the semi-insulating property of the single VZTO film.

In order to investigate the origin of abnormal electrical properties of the VZTO films, which depend on channel structure, XPS measurements were carried out using an Al K $\alpha$  X-ray source. Before XPS measurements, the surfaces of the films were slightly sputtered by Ar ions to eliminate artifacts from surface contamination. Figure 2 illustrates the XPS spectra



**Figure 2.** (a) O 1s, (b) Sn 3d<sub>5/2</sub>, and (c) V 2p<sub>3/2</sub> XPS spectra of ZTO, VZTO/ZTO, and VZTO films.

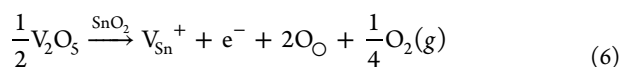
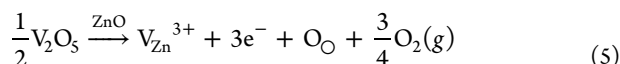
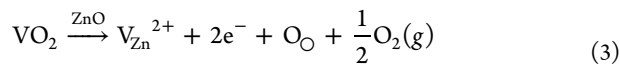
of the O 1s, Sn 3d<sub>5/2</sub>, and V 2p<sub>3/2</sub> core levels obtained from the ZTO, VZTO, and VZTO on ZTO. The peak centered at 284.5 eV was assigned as the binding energy of the C 1s peak for C–C bonds to calibrate the binding energies of photoelectrons. As shown in Figure 2a, the O 1s spectra were deconvoluted into three peaks centered at binding energies of 530.1 ± 0.1, 531.3 ± 0.1, and 532.1 ± 0.1 eV. The lowest peaks (M–O) at binding energies of 530.1 ± 0.1 eV are attributed to the O<sup>2-</sup> ions that are fully coordinated with metal cations.<sup>24,25</sup> The medium peaks at 531.3 ± 0.1 eV are from the oxygen vacancies ( $V_O$ ), and the peaks at 532.1 ± 0.1 eV are from hydroxyl groups (O–H).<sup>24,25</sup> As listed in Table S1 in the Supporting Information, the relative peak area of  $V_O$  in the ZTO film was 13.7% and reduced to 5.3% and 4.5% in VZTO and VZTO/ZTO films, respectively. This result indicates that the V ions act as a  $V_O$  suppressor, as expected.  $V_O$  is well-known as the carrier generator in many oxides, because  $V_O$  can be ionized by thermal energy and can simultaneously eject carrier electrons into the channel layer.<sup>4,27,34</sup> Based on thermodynamics, the carrier concentration originating from  $V_O$  increases with  $V_O$  concen-



**Figure 3.** Change in transfer curves of the ZTO TFT under (a) negative-bias stress (NBS) and (b) illumination stress (IS) only and negative bias illumination stress (NBIS). Hysteresis loop of (c) ZTO TFT under IS only or NBIS for 5000 s. As for the VZTO/ZTO bilayer TFT, (d) NBS, (e) IS only and NBIS, and (f) hysteresis. NBS, IS, and NBIS conditions are as follows: (i) NBS:  $V_G = -30$  V, duration time = 5000 s; (ii) IS: wavelength = 550 nm, power density =  $0.1 \text{ mW cm}^{-2}$ , duration time = 5 min; (iii) NBIS:  $V_G = -30$  V, wavelength = 550 nm, power density =  $0.1 \text{ mW cm}^{-2}$ , duration time = 5000 s.

tration ( $[V_O]$ ). Hence, if  $V_O$  provides the dominant contribution to the total carrier concentration ( $n_T$ ) of the VZTO films, the carrier concentration of the single VZTO film is similar with or larger than that of the VZTO film in the bilayer when considering the tendency of  $[V_O]$  in the VZTO films. However, this explanation is opposite to our observation. Thus, we took into account the change in the chemical bonding of Sn and V atoms, as shown in Figure 2b and 2c.

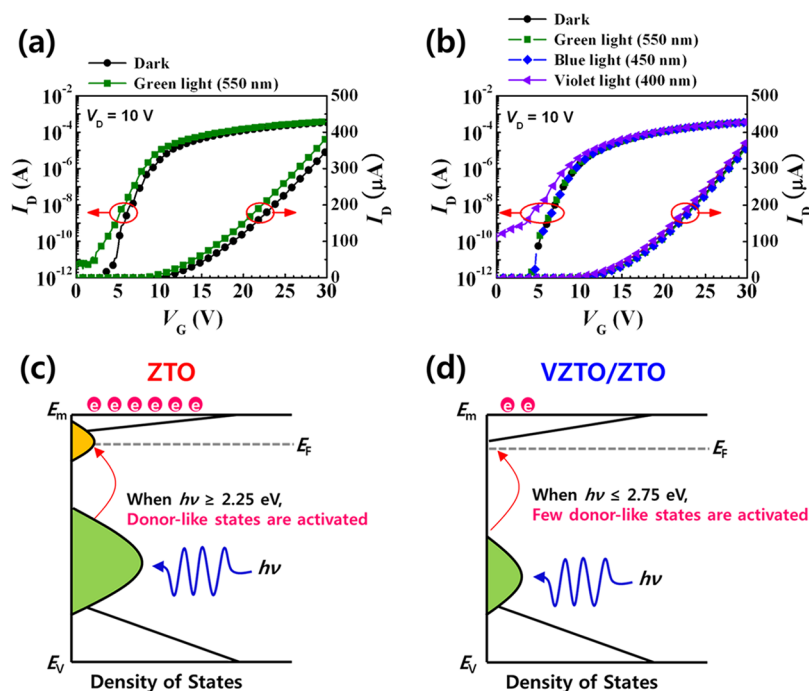
Sn and V atoms in oxides appear as multivalent ions. Sn has two ionic forms ( $\text{Sn}^{4+}$  and  $\text{Sn}^{2+}$ ), and V has four ionic forms ( $\text{V}^{5+}$ ,  $\text{V}^{4+}$ ,  $\text{V}^{3+}$ , and  $\text{V}^{2+}$ ). The Sn  $3d_{5/2}$  spectra in Figure 2b and Table S2 (Supporting Information) revealed that Sn ions in the semi-insulating VZTO film (single VZTO film) consist of  $\sim 10\%$   $\text{Sn}^{2+}$  ions and  $\sim 90\%$   $\text{Sn}^{4+}$  ions. On the other hand, the semiconducting VZTO film in the VZTO/ZTO bilayer shows predominant chemical bonding of  $\text{Sn}^{4+}$  cations ( $>98\%$ ). In addition, it is very interesting that V  $2p_{3/2}$  spectra between single layers and bilayers are quite different, as shown in Figure 2c. Based on literature sources compiled in the NIST database, V  $2p_{3/2}$  spectra were fitted with two fitting curves, corresponding to  $\text{V}^{4+}$  and  $\text{V}^{5+}$ . The semiconducting VZTO film on the ZTO shows the cation distribution of 17%  $\text{V}^{4+}$  and 83%  $\text{V}^{5+}$ , while the V cations in the semi-insulating single VZTO film consist of 37%  $\text{V}^{4+}$  and 63%  $\text{V}^{5+}$  ions (see Table S3 in the Supporting Information). These results imply that the significant reduction of Sn and V metal ions is strongly related to the semi-insulating behavior of the VZTO film. Recently, we reported that a weak broad shoulder peak located at  $33\text{--}34^\circ$  in grazing incidence X-ray diffraction (GIXRD) data of the ZTO films were originated from the amorphous-like phase including locally small crystalline phase.<sup>39</sup> Thus, the concept for the substitution of V ions into Zn or Sn sites can be partly considered as the following reactions:



where  $V_M$  is a vanadium atom that occupies a M site ( $M = \text{Zn}, \text{Sn}$ ) and is not an oxygen vacancy, which is expressed as  $V_O$ . Also, the superscript “ $\times$ ” indicates neutrality. The above reactions (except reaction 4) show that aliovalent V doping in substitutional sites can give carrier electrons, regardless of whether V occurs as  $\text{V}^{4+}$  or  $\text{V}^{5+}$ . This may be effective, to some degree, in the semiconducting VZTO on ZTO. For the single VZTO film, the contribution of the ionization of V atoms to  $n_T$  is much less than that in the VZTO on ZTO, since the content of  $\text{V}^{5+}$  ions (which can provide more electrons than  $\text{V}^{4+}$  ions) decreases from 83% in the VZTO on ZTO to 59% in the single VZTO film. Moreover, much larger reduction of Sn ions from  $\text{Sn}^{4+}$  to  $\text{Sn}^{2+}$  is observed in the single VZTO film, given the presence of the shoulder peak coming from  $\text{Sn}^{2+}$  bonding in Figure 2b. This leads to the generation of holes, which is described as follows:



Thus, electron charge carriers in the single VZTO film can be compensated by these holes. Meanwhile, there is little reduction of Sn ions from  $\text{Sn}^{4+}$  to  $\text{Sn}^{2+}$  in the VZTO on ZTO. Accordingly, the VZTO/ZTO bilayer TFT shows quite good TFT performance, unlike the single VZTO TFT. This



**Figure 4.** Change in transfer curves of (a) ZTO and (b) VZTO/ZTO TFTs under illumination stress (IS) at various wavelengths ( $\lambda = 400, 450,$  and  $550$  nm) with a fixed power density ( $0.1 \text{ mW cm}^{-2}$ ). Schematic illustrations of the change in the subgap states of (c) ZTO and (d) VZTO/ZTO films under IS.

phenomenon was clearly interpreted with the aid of XPS analysis of Sn and V cation bonding.

**3.2. Dramatically Improved Stability of VZTO/ZTO Bilayer TFT.** Figures 3a and 3d show the results of negative bias stress (NBS) tests for the ZTO single-layer and VZTO/ZTO bilayer TFTs, respectively, where a  $V_G$  value of  $-30$  V was applied for a total duration of 5000 s under dark conditions. The stress was interrupted several times to measure transfer characteristics at a  $V_D$  of 10 V. Threshold voltage shift ( $\Delta V_{th}$ ) values of the ZTO and VZTO/ZTO TFTs were estimated to be 1.0 and  $<0.5$  V, respectively, showing better stability of the bilayer TFT than that of the single-layer TFT. The negative  $\Delta V_{th}$  under NBS is attributed to the screen effect induced by positive charges at the semiconductor/insulator interface or in the near-interface bulk. These positive charges mainly originated from the donor-like states such as neutral oxygen vacancies ( $\text{V}_O$ ), which can capture the minority hole carriers accumulated at/near the interface by NBS and can be positively charged ( $\text{V}_O^{2+}$ ).<sup>8,27</sup> Thus, the reduced  $\Delta V_{th}$  under NBS in the bilayer TFT indicates decreased donor-like states at the interface and/or near-interface bulk. However, the  $\Delta V_{th}$  under NBS is relatively trivial.

In order to investigate more practical applicability of the VZTO/ZTO bilayer TFT and the stark difference in stability, the transfer characteristics of ZTO and VZTO/ZTO TFTs were measured under negative-bias illumination stress (NBIS). For the NBIS tests, both an NBS with  $V_G = -30$  V and an illumination stress (IS) with a wavelength of 550 nm (green) and a power density of  $\sim 0.1 \text{ mW cm}^{-2}$  were simultaneously applied to the two TFT devices. Figures 3b and 3e show the change of transfer characteristics of ZTO and VZTO/ZTO TFTs, respectively, when each device is in darkness, under IS, and NBIS. As shown in Figure 3b, there was a severe change in the transfer characteristics of the ZTO TFT under NBIS. Furthermore,  $\Delta V_{th}$  values of the ZTO TFT under IS only and

under NBIS for 5000 s were  $-2.1$  V and  $-7.3$  V, respectively. The negative  $\Delta V_{th}$  under IS can be attributed to the creation of both charged donor-like defects (e.g.,  $\text{V}_O^{2+}$ ) and excess carrier electrons by photoexcitation of donor-like defects (e.g.,  $\text{V}_O$ ).<sup>7-11,27</sup> Moreover, the SS value of ZTO TFT is severely degraded from  $0.24 \text{ V dec}^{-1}$  in darkness, to  $1.1 \text{ V dec}^{-1}$  after IS, and even to  $2.6 \text{ V dec}^{-1}$  after the NBIS test. The severe degradation of SS values induced by IS indicates that a large amount of the photocreated charged defects are at/near the interface and have an energy level above the flat-band Fermi level ( $E_{F0}$ ).<sup>9,11</sup> In addition to the creation of ionized donor-like defects by IS, the charged donor-like defects in the bulk can migrate to the interface/near-interface due to the negative bias during NBIS,<sup>8,11</sup> leading to a much larger  $\Delta V_{th}$  and more severe degradation of SS values than the case of IS or NBS. This degradation mechanism, called the “defect ionization/migration model” here, is supported by the significant change in hysteresis behaviors of ZTO TFTs after IS and NBIS, as shown in Figure 3c. Here, the symbol  $\Delta V_{Hys}$  is defined as the maximum shift of  $V_G$  at the same  $I_D$  in the hysteresis loop. The  $\Delta V_{Hys}$  value is known to be related to the interface trap density and/or near-interface bulk trap density above  $E_{F0}$ .<sup>18,40</sup> Furthermore, the  $\Delta V_{Hys}$  value of the ZTO TFT significantly increased from 2.0 V under IS to 5.1 V after NBIS for 5000 s. This photobias-induced hysteresis creation indicates that a significant increase in the interface trap density and/or near-interface bulk trap density in the ZTO TFT occurred due to NBIS.<sup>40</sup> Unlike the poor stability of the ZTO TFT under IS and NBIS, the VZTO/ZTO bilayer TFT showed a dramatically improved stability under both IS and NBIS. As shown in Figure 3e, the transfer characteristics of the VZTO/ZTO bilayer TFT under IS changed only slightly, compared to that in darkness. Even after NBIS for 5000 s, there was a very small  $\Delta V_{th}$  value of 0.9 V and SS values were almost unchanged, as is apparent in Figure 3f. In addition, the  $\Delta V_{Hys}$  values of the VZTO/ZTO TFT were

almost unchanged after NBIS for 5000 s ( $\sim 0.3$  V). This indicates that the VZTO/ZTO has few donor-like defects, which can be activated by a photon energy of 550-nm wavelength (2.25 eV) to affect the transfer characteristics of the TFT.

To obtain more insight into the defect level in the bandgap, we performed IS tests with an illumination source of various wavelengths, as shown in Figures 4a and 4b. The severe SS degradation of the single ZTO TFT starts with green light energy of 550 nm (2.25 eV). On the other hand, the VZTO/ZTO bilayer TFT is almost unchanged under illumination stress, even with a blue wavelength of 450 nm (2.75 eV), and exhibits SS degradation by the irradiation of near UV photon energy ( $\sim 400$  nm), which is near the optical bandgap of the single ZTO film (3.0 eV; see Figure S1 in the Supporting Information). This indicates that the VZTO/ZTO bilayer has few donor-like states that can be activated and donate charge carriers by photons with energies of  $\leq 2.75$  eV, as illustrated in Figure 4d. In contrast, the single ZTO film has a great deal of the subgap states that can be activated by low-energy irradiation (Figure 4c).

## 4. CONCLUSION

We have suggested a novel channel design to incorporate multivalent metal cations for the fabrication of oxide thin-film transistors (TFTs) with both high electrical performance and high stability. The addition of multivalent V atoms into indium-free ZTO films induced a dual electrical performance, depending on the channel structure. The VZTO/ZTO channel structure exhibited relatively good TFT performance without degradation of mobility, while the VZTO TFT had no typical TFT transfer characteristics. XPS analyses revealed that the semi-insulating behavior of the single VZTO film was attributed to the existence of more  $V^{4+}$  and a significant reduction of  $Sn^{4+}$  to  $Sn^{2+}$  ions. In NBIS tests, it was found that the VZTO/ZTO TFT had much better stability than the single ZTO TFT. Under IS tests of various wavelengths, dramatically improved stability of the VZTO/ZTO TFT was observed due to the reduction of donor-like states that can be positively ionized by blue and green illumination. Therefore, doping with a multivalent elemental vanadium in the ZTO film and the use of a VZTO/ZTO channel structure are expected to provide a feasible strategy for highly reliable performance of the oxide-based TFT devices.

## ■ ASSOCIATED CONTENT

### Supporting Information

O 1s XPS results for ZTO, VZTO/ZTO, and VZTO films; Sn  $3d_{5/2}$  XPS results for ZTO, VZTO/ZTO, and VZTO films; V  $2p_{3/2}$  XPS results for VZTO/ZTO and VZTO films; transmittance of the ZTO and VZTO films on glass substrates. This material is available free of charge via the Internet at <http://pubs.acs.org>.

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### Notes

The authors declare no competing financial interest.

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## ■ ABBREVIATIONS

AOS = amorphous oxide semiconductor  
AMLCD = active matrix liquid crystal display  
AMOLED = active matrix organic light-emitting diode  
ZTO = zinc tin oxide  
VZTO = vanadium-doped zinc tin oxide  
XPS = X-ray photoelectron spectroscopy  
NBS = negative-bias stress  
IS = illumination stress  
NBIS = negative-bias illumination stress

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